

INFORMATION DISCLOSURE CITATION
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Docket Number (Optional)

86177-23

Application Number

Applicant(s)

R. Toutant et al.

Filing Date

Group Art Unit

10921 U.S. PTO
1091994017
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U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
Ams	1.	U.S. 6,285,679 B1	04/09/2001	Dally et al.			
Ams	2.	U.S. 6,044,080	28/03/2000	Antonov			
Ams	3.	U.S. 5,541,914	30/07/1996	Krishnamoorthy et al.			
Ams	4.	U.S. 5,461,624	24/10/1995	Mazzola			
Ams	5.	U.S. 5,526,352	11/06/1996	Min et al.			
Ams	6.	U.S. 5,598,408	28/01/1997	Nickolls et al.			
Ams	7.	U.S. 6,205,532 B1	20/03/2001	Carvey et al.			

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

Ams	8.	Switch Clusterings: A Breakthrough for Scalable and Affordable LANs; Cisco Systems; 2001 Cisco Systems, Inc. PP 1 to 6; http://www.cisco.com ; retrived September 20, 2001.
Ams	9.	Multi-Purpose Client-Programmable Routers; Link Interface Node for External Systems; Interface Concept; http://www.interfaceconcept.com ; retrieved September 20, 2001.

EXAMINER

Anthony Sol

DATE CONSIDERED

7/14/05

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*EXAMINER INITIAL	OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)	
AmS	10.	Optical Fiber Interconnection for the Scalable Parallel Computing System; Ge Zhou, Yimo Zhang and Wei Liu; Proceedings of the IEEE, Vol. 88, No. 6, June 2000;
AmS	11.	Design of Giga-Bit/s Optical Interconnection Network for Computer Cluster; Ge Zhou, Wen-Cal Jing, Yi-Mo Zhanbg, Feng Hua, Jin-Dong Tian, Yian-Ling Chen and Wei Liu; College of Precision Instrument and Opto-electronic Engineering; 1998 IEEE; PP 194 to 199;
AmS	12.	0.95 - 3.125 Gbps 140x140-Lane, Multi-Rate Space Switch with Clock and Data Recovery at Each Serial Input; Velio Communications, Inc.; 2001; PP 1 to 36;
AmS	13.	Gigabit Switch Using Free-Space and Parallel Optical Data Links for a PCI-Based Workstation Cluster; J. Ekman, P. Chandramani, P. Guy, X. Wang, F. Kiamilev; 2000 IEEE; PP 494 and 495;
AmS	14.	A complete Sub-System of Parallel Optical Interconnects for Telecom Applications; M. Niburg, P. Eriksen; K. Gustafsson, H. Hesselbom, G. Palmkog, M. Robertsson, N. Tan, K. Akermark, A. Djupsjobacka; Ericsson Components AB, Microelectronics Division, Corporate Research Centra; 1996 Electronic Components and Technology Conference; 1996 IEEE;
AmS	15.	Optical Interconnects in Parallel Computer RWC-1; Junichi Shimada; RWCP Research Institute; 1999 IEEE PP 2 and 3;
AmS	16.	Parallel Optical Network Interconnects; A.F.J. Levi; University of Southern California; 1999 IEEE; PP 693 and 694.

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